

ABSTRACT OF THE DISCLOSURE

Offset voltage correction apparatus and method correct an input offset voltage in a comparator circuit with a very high accuracy. To detect an input offset voltage in a comparator, a counter circuit performs both up-count and down-count operations on a clock signal. An offset voltage correction circuit obtains a value (counter value) of a counter signal corresponding to an input offset voltage provided at the transition in a comparison signal from "1" to "0" and a value (counter value) of the counter signal corresponding to an input offset voltage provided at the transition from "0" to "1." The offset voltage correction circuit performs a predetermined arithmetic operation using the counter values. The offset voltage in the comparator is corrected in accordance with this arithmetic operation result.